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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known	
Application Number	09/239,898
Filing Date	January 29, 1999
First Named Inventor	Seyyedy, Mirmajid
Group Art Unit	2133
Examiner Name	Lamarre, Guy <i>SG</i>
Sheet 1 of 1	Attorney Docket No: 303.550US1

OTHER DOCUMENTS – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>SG</i>		"16Mb Double Data Rate Synchronous Graphics RAM", <u>IBM Corporation</u> , IBM0616328RL6A,(December 1997),pp. 1-53	
<i>SG</i>		"16Mbit DDR SGRAM, 128K x 32Bit x 4 Banks Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Revision 0.6", <u>Samsung Electronics</u> , KM432D5131,(April 1998),pp. 1-48	

EXAMINER *SG*

DATE CONSIDERED *4/22/05*

Substitute Disclosure Statement Form (PTO-1449)

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached